

FROM RESEARCH TO INDUSTRY

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Automated Program Analysis for Security : What About the Attacker ?

Commissariat à l'énergie atomique et aux énergies alternatives - www.cea.fr



DILS/LSL : Lab. For Software Security and Safety

- rigorous tools for building high-level quality software
- second part of V-cycle
- automatic software analysis
- mostly source code





Software Analyzers

















The BINSEC Group: ADAPT FORMAL METHODS TO BINARY-LEVEL SECURITY ANALYSIS





https://binsec.github.io/











TEAM WORK SINCE 2012 [+ UGA, LORIA, INRIA]















































This Talk in a Nutshell

- Program-level security is a key aspect [yet, a single bug can ruin everything]
- Program Analysis (PL) and Formal Methods come from critical safety needs
 - Damn good there (in the hands of experts)
 - Allow to prove the absence of bugs, or find them thoroughly
- Now : a move from safety concerns to security concerns
- **Questions:** how does security differ from safety?
- Answer : the attacker
- This talk: share some insights and results from the BINSEC team @DILS





THE SECURITY GAME

- The defender: try to secure the whole system ٠
- The attacker: try to abuse the system ٠
 - Why: for fun & profit ٠
 - How: by taking advantage of a single flaws (bugs) •
- The user: collateral damage















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Dissymetric battlefield Advantage to the attacker (in most cases)











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most attacks come from
 implementation bugs
 bugs are inevitable













Dissymetric battlefield Advantage to the attacker (in most cases) most attacks come from
implementation bugs
bugs are inevitable

Quite depressing ...

What if software could be immune to large classes of bugs? What if bugs could be found (and patch) automatically?





- Introduction [The Sad Truth]
- Reasoning about programs [A New Hope]
- What about the attacker? [The Evil Returns]
- Some results [Hard Battle In Progress]
- Conclusion, Take away and Disgression



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THEN CAME FORMAL METHODS

- Between Software Engineering and Theoretical Computer Science
- Goal = proves correctness in a mathematical way





Success in (regulated) safety-critical domains





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Success in (regulated) safety-critical domains





A DREAM COME TRUE ... IN CERTAIN DOMAINS

Ex : Airbus

Verification of

- runtime errors [Astrée]
- functional correctness [Frama-C *]
- numerical precision [Fluctuat *]
- source-binary conformance [CompCert]
- ressource usage [Absint]

* : by CEA DILS/LSL







Simple example

• Goal : prove result is positive





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They knew it was impossible, so they did it anyway



Cannot have analysis that

- Terminates
- Is perfectly precise

On all programs

Answers

- Forget perfect precision: bugs xor proofs
- Or focus only on « interesting » programs
- Or put a human in the loop
- Or forget termination





- Weakest precondition calculi [1969, Hoare]
- Abstract Interpretation [1977, Cousot & Cousot]
- Model checking [1981, Clarke Sifakis]



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Formal methods zoo : so many of them, so little time for the talk





Bounded verification – bug finding

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List Formal methods zoo : So

Formal methods zoo : so many of them, so little time for the talk







WHAT ABOUT USING THEM IN SECURITY ?





The SMACCMCopter: 18-Month Assessment

The SMACCMCopter flies:

- Stability control, altitude hold, directional hold, DOS detection.
- GPS waypoint navigation 80% implemented.

Air Team proved system-wide security properties:

- The system is memory safe.
- The system ignores malformed messages.
- The system ignores non-authenticated messages.
- All "good" messages received by SMACCMCopter radio will reach the motor controller.

• Red Team:

• Found no security flaws in six weeks with full access to source code.

Penetration Testing Expert:

The SMACCMCopter is probably "the most secure UAV on the planet" Open source: autopilot and tools available

Good Idea !











Formally hardened UAV

Developped from scratch

Survives 6 weeks of red team attacks with full code & doc access

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from http://smaccmpilot.org







End of the story ?



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End of the story ? Not yet ...





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EXAMPLE: side channel attacks





- Yes, sometimes
- Come from the implementation





EXAMPLE 1: side channel attacks

private char[4] secret;

boolean CheckPassword (char[4] input) {
 for (i=0 to 3) do
 if(input[i] != secret[i]) then
 return false;
 endif
 endfor
 return true;
}



• Can you retrieve the secret with blackbox access?







EXAMPLE 2: fault injection attacks

private char[4] secret;

```
void CheckandPrint (char[4] input) {
```

If (input == secret) then get-access() else stop() ;





Can you get access without knowning secret?



STANDARD PROGRAM ANALYSIS IS NOT (always) ENOUGH

Related to the safety vs security question



Introducing the attacker









ATTACKER in Standard Program Analysis



• We are reasoning worst case: seems very powerful!





ATTACKER in Standard Program Analysis



- We are reasoning worst case: seems very powerful!
- Still, our current attacker plays the rules: respects the program interface
 - Can craft very smart input, but only through expected input sources





ATTACKER in Standard Program Ana

- We are reasoning worst case: seems very
- Still, our attacker plays the rules: respects
 - Can craft very smart input, but only through expected



- What about someone who really do not play the rules?
 - Side channel attacks
 - Micro-architectural attacks
 - Fault injections











HOW TO TAKE THE ATTACKERS INTO ACCOUNT ?



Expressivity vs How to handle it efficiently













- Introduction [The Sad Truth]
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- Introduction [The Sad Truth]
- Reasoning about programs [A New Hope]
- What about the attacker? [The Evil Returns]
- Some results [Hard Battle In Progress]
 - detour : BINSEC
 - Taking the attacker into account in BINSEC
- Conclusion, Take away and Disgression











WHY GOING DOWN TO BINARY-LEVEL SECURITY ANALYSIS?





EXAMPLE: COMPILER BUG (?)



- Optimizing compilers may remove dead code
- pwd never accessed after memset
- Thus can be safely removed
- And allows the password to stay longer in memory

Security bug introduced by a non-buggy compiler

void getPassword(void) {
 char pwd [64];
 if (GetPassword(pwd,sizeof(pwd))) {
 /* checkpassword */
 }
 memset(pwd,0,sizeof(pwd));
}

OpenSSH CVE-2016-0777

- secure source code
- insecure executable



EZ.

BINSEC: brings formal methods to binary-level security analysis

Source Code

Executab



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COTS

Auteur

Ransomware



SYMBOLIC EXECUTION (Godefroid 2005)



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INTERMEDIATE REPRESENTATION [CAV'11]

Binsec intermediate representation

inst := $lv \leftarrow e \mid goto e \mid if e then goto e$ $lv := var \mid @[e]_n$ $e := cst \mid lv \mid unop e \mid binop e e \mid e ? e : e$

 $\begin{array}{rcl} \text{unop} & := & \neg \mid - \mid \text{uext}_n \mid \text{sext}_n \mid \text{extract}_{i..j} \\ \text{binop} & := & \text{arith} \mid \text{bitwise} \mid \text{cmp} \mid \text{concat} \\ \text{arith} & := & + \mid - \mid \times \mid \text{udiv} \mid \text{urem} \mid \text{sdiv} \mid \text{srem} \\ \text{bitwise} & := & \land \mid \lor \mid \oplus \mid \text{shl} \mid \text{shr} \mid \text{sar} \\ \text{cmp} & := & = \mid \neq \mid >_u \mid <_u \mid >_s \mid <_s \end{array}$

Multi-architecture

x86-32bit – ARMv7

lhs := rhs

- goto addr, goto expr
- ite(cond)? goto addr

- Concise
- Well-defined
- Clear, side-effect free





INTERMEDIATE REPRESENTATION



- Concise
- Well-defined
- Clear, side-effect free

$$\begin{array}{c|c} \hline 81 \text{ c3 57 1d } 00 \text{ 00} \end{array} \xrightarrow[]{x86 reference} \hline \text{ADD EBX 1d57} \end{array}$$





BINSEC / SOME HIGHLIGHTS

Vulnerability finding in open source code

- Fuzzing + program analysis
 Use-after-free, patch issues
- 15 CVE, 37 bugs
- Black Hat 2020, RAID 2020



- Help reverse advanced malware
- **Obfuscation detection & simplif**
- 12 min for +400k instr.
- Black Hat EU 2016, IEEE S&P 2017



- Verify cryptographic implementations
- Side channels and Spectre attacks
- Check 350+ crypto implementations
- 3 vulnerabilities introduced by compilers
- report possible flaws in standard protections
- IEEE S&P 2020, NDSS 2021





- Help handle inline assembly
- Verification-oriented decompilation
 - Tested on all Debian C+asm chunks
- Interface conformance checking
 - Found 100's of errors
 - propose patch, 10's got accepted





- Security scenarios
 - Vulnerability analysis and automated exploit generation
 - Side channel attacks
 - Speculative side channel attacks
 - Physical fault injection
 - Bug priorisation



Basic power





Vulnerability finding with symbolic execution (Godefroid et al., Cadar et al., Sen et al., etc.)





Vulnerability finding with symbolic execution (Godefroid et al., Cadar et al., Sen et al., etc.)





Vulnerability finding with symbolic execution (Heelan, Brumley et al.)



Intensive path exploration
 Target critical bugs
 Directly create simple exploits





Find a needle in the heap!





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Can compare executions





« True » security properties (a.k.a. hyper-properties)

Information leakage

Properties over pairs of executions









SECURING CRYPTO-PRIMITIVES -- [S&P 2020] (Lesly-Ann Daniel)



timing attacks cache attacks (secret-erasure)

Relational symbolic execution
 Follows paires of execution
 Check for divergence





SECURING CRYPTO-PRIMITIVES

-- [S&P 2020] (Lesly-Ann Daniel)



		#Instr static	#Instr unrol.	Time	CT source	Status	₩	Comment
utility	ct-select ct-sort	735 3600	767 7513	.29 13.3	Y Y	21× X 18× X	21 44	1 new 🗡 2 new 🗡
BearSSL	aes_big des_tab	375 365	873 10421	1574 9.4	N N	× ×	32 8	-
OpenSSL tls-remove-	pad-lucky13	950	11372	2574	Ν	X	5	-
Total		6025	30946	4172	-	42 × X	110	-

Relational symbolic execution
 Follows paires of execution
 Check for divergence
 Sharing, dedicated preprocessing

- 397 crypto code samples, x86 and ARM
- New proofs, 3 new bugs (of verified codes)
- 600x faster than prior workl





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Can observe more





Speculative executins and Spectre attacks

Spectre attacks (2018)

- Exploit speculative execution in processors
- Affect almost all processors
- Attackers can force mispeculations: transient executions
- Transient executions are reverted at architectural level
- But not the microarchitectural state (e.g. cache)





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Challenge !

- Counter-intuitive semantics
- Path explosion:
 - Spectre-STL: all possible

load/store interleavings !

Needs to hold at binary-level

Path explosion for Spectre-STL on Litmus tests (328 instr.)





List Challenge !

- Counter-intuitive semantics
- Path explosion:
 - Spectre- Extends M into M_spec
 - Property over paires

binary-level

Key concepts : $M \models \varphi$

 \blacksquare *M* : semantic of the program

load

- φ : property to be checked
- $\blacksquare \models$: algorithmic check

Path explosion for Spectre-STL on Litmus tests (328 instr.)







Challenge !







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Can act on the execution







Context

- □ Many techniques and tools for security evaluations.
- Usually consider a weak attacker, able to **craft smart inputs**.
- Real-world attackers are more powerful: various attack vectors + multiple actions in one attack.



Micro-architectural attacks

Man-At-The-End attacks









- Waht about advanced attackers ?
- Recent work :
 - support for attacker model
 - Fault injection-like capabilities



• Goal

- Help security evaluators
- Help mitigation designers

WooKey bootloader

- 1. Find known attacks
 - attacks
- 2. Evaluate countermeasures from prior work
- 3. Find previously unreported attack path
- 4. Propose and check mitigation











Waht about advanced attackers ? Recent work : ۲ WooKey bootloader support for attacker model 1. Find known attacks Fault injection-like capabilities m prior work Extends M into M_spec ack path Property over paires Key concepts : $M \models \varphi$ Goal M : semantic of the program Help security evaluators Path explosion Help mitigation designe • φ : property to be checked **Dedicated optimizations** $\blacksquare \models$: algorithmic check







Security scenarios using different fault models

CRT-RSA: [1]

- \Box basic vulnerable to 1 reset \rightarrow OK
- □ Shamir (vulnerable) and Aumuler (resistant) → TO

Secret-keeping machine: [2]

- □ Linked-list implementation vulnerable to 1 bit-flip in memory \rightarrow OK
- □ Array implementation resistant to 1 bit-flip in memory \rightarrow OK
- □ Array implementation vulnerable to 1 bit-flip in registers \rightarrow OK

Secswift countermeasure: IIvm-level CFI

protection by STMicroelectronics [3]

 □ SecSwift impementation [4] applied to
 VerifyPIN_0 → early loop exit attack with 1 arbitrary data fault or test inversion in valid CFG

 Puys, M., Riviere, L., Bringer, J., Le, T.h.: High-level simulation for multiple fault injection evaluation. In: Data Privacy Management, Autonomous Spontaneous Security, and Security Assurance. Springer (2014)
 Dullien, T.: Weird machines, exploitability, and provable unexploitability. IEEE Transactions on Emerging Topics in Computing (2017)

[3] de Ferrière, F.: Software countermeausres in the llvm risc-v compiler (2021),

https://open-src-soc.org/2021-03/media/slides/3rd-RISC-V-Meeting-2021-03-30-15h00-Fran%C3%A7ois-de-Ferri%C3%A8re.pdf

[4] Lacombe, G., Feliot, D., Boespflug, E., Potet, M.L.: Combining static analysis and dynamic symbolic execution in a toolchain to detect fault injection vulnerabilities. In: PROOFS WORKSHOP (SECURITY PROOFS FOR EMBEDDED SYSTEMS) (2021)

Case study

WooKey bootloader: secure data storage by ANSSI, 3.2k loc. Goals:

- 1. Find known attacks (from source-level analysis)
 - a. Boot on the old firmware instead for the newest one [1]
 - b. A buffer overflow triggered by fault injection [1]
 - c. An incorrectly implemented countermeasure protecting against one test inversion [2]
- 2. Evaluate countermeasures from [1]
 - a. Evaluate original code → We found an attack not mentioned before
 - b. Evaluate existing protection scheme [1] (not enough)
 - c. Propose and evaluate our own protection scheme

[1] Lacombe, G., Feliot, D., Boespflug, E., Potet, M.L.: Combining static analysis and dynamic symbolic execution in a toolchain to detect fault injection vulnerabilities. In: PROOFS WORKSHOP (SECURITY PROOFS FOR EMBEDDED SYSTEMS) (2021)

[2] Martin, T., Kosmatov, N., Prevosto, V.: Verifying redundant-check based countermeasures: a case study. In: Proceedings of the 37th ACM/SIGAPP Symposium on Applied Computing. (2022)







- Security scenarios
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Looks for strong attacks





Bug Priorization / Criticity Evaluation [CAV 18, CAV 21, FMSD 22, POPL 24, PLDI 24]

- Too many bugs. Which ones are relevant ?
- Defender can focus on these ones
- From the attacker point of view
 - replicability
 - Level of control
 - •



list ^{CE2 tech}

Bug Priorization / Criticity Evaluation [CAV 18, CAV 21, FMSD 22, POPL 24, PLDI 24]

- Too many bugs. Which ones are relevant ?
- Defender can focus on these ones
- From the attacker point of view
 - replicability
 - Level of control

- Especially, bugs reported by standard program analysis may be poorly replicable
 - Ex : fault injection with very specific values
 - Ex : bugs depending on uninitialized memory
 - Ex : bugs depending on random values
 - •
 - •

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list

Bug Priorization / Criticity Evaluation [CAV 18, CAV 21, FMSD 22, POPL 24, PLDI 24]

	attacker .		РуАвd ^O	Binsec/RSE
	environment	unkn	own 170	273
Choose a threat Model		not vulnerable (0 in	out) 4414	4419
Partition input into controlled input a and		vulnerable (≥ 1 in	put) 226	118
	LOT BUT	≥ 0.00	01% 226	118
$(a, x) \vdash \ell$ means "with inputs a and x, the program	BD CHT	$\geq 0.$	01% 209	118
	A LE	\geq	0.1% 173	118
Reachability of Robust Reachability	>_	\geq	.0% 167	118
location ℓ of ℓ	Guaranteed	\geq	5.0% 166	118
$\exists a, \underline{x}.(a, \underline{x}) \vdash \ell \qquad \exists a. \forall \underline{x}.(a, \underline{x}) \vdash \ell$	Guardineed	≥ 10	0.0% 118	118
		≥ 50	0.0% 118	118
		10	0.0% 118	118



- *M* : semantic of the program
- φ : property to be checked
- $\blacksquare \models$: algorithmic check

Modify the satisfaction relation



BINSEC

170

3921

719 +

Qemu

243

4398

169



- Security scenarios
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 - Physical fault injection
 - Bug priorisation
 - BONUS : reverse of malware



Craft its own code





eg: **7y² - 1 ≠ x²**

(for any value of x, y in modular arithmetic)

eax, ds:X

ecx. ds:Y

ecx, ecx

ecx, 7

ecx, 1

eax, eax

ecx, eax

<dead_addr>

mov

mov

imul

imul

imul

sub

cmp

iz

Another Line of attack : ADVERSARIAL BINARY CODE



address	instr
80483d1	call +5
80483d6	pop edx
80483d7	add edx, 8
80483da	push edx
80483db	ret
80483dc	.byte{invalid}
80483de	[]



universite

- self-modification
- encryption
- virtualization
- code overlapping
- opaque predicates
- callstack tampering

•





FOCUS Reverse: THE XTUNNEL MALWARE

-- [BlackHat EU 2016, S&P 2017, ACSAC 2019, CCS 2022]





Two heavily obfuscated samples

Many opaque predicates

Goal: detect & remove protections

- Identify 40% of code as spurious
- Fully automatic, < 3h [now : 12min]

Backward-bounded SE
 + dynamic analysis

	C637 Sample #1	99B4 Sample #2		
#total instruction	505,008	434,143		
#alive	+279,483	+241,177		

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- Taking the attacker into account in program analysis
- New scientific challenges grounded in real security
 - Fruitful Useful Fun







https://binsec.github.io/









- Advanced automated reasoning as a game changer in cybersecurity
 - Leverage and adapt best methods from safety-critical domains
 - Fruitful !
 - Beware of scalability and learning curve
- Yet, security is not safety
 - the attacker must be taken into account
 - field in progress
- Toward truly security-oriented program analysis !

https://binsec.github.io/



THANK YOU

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